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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: **Fang, et al.**

Serial No.: **10/762,445**

Filed: **January 22, 2004**

For: **Structure and Method for Low VSS
Resistance and Reduced DIBL in a Floating
Gate Memory Cell**

Art Unit: 2891

Examiner: Sarkar, Asok K.

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
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In this Information Disclosure Statement and pursuant to 37 C.F.R. §§ 1.56 and 1.97, Applicants hereby disclose to the Patent Office patents, publications or other information of which Applicants are aware. A list of U.S. published patent application numbers is included in Form 1449 submitted herewith.

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No fee is believed due for the filing of this Disclosure Statement.

Respectfully submitted,
Farjami & Farjami LLP

Dated: 11/17/05

By: 

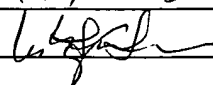
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FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 0180192	SERIAL NO. 10/762, 445
	APPLICANTS: Fang, et al.	
	FILING DATE: 1/22/2004	GROUP ART: 2891

U.S. PATENT DOCUMENTS							
Exam. Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	1	6,531,347	3/2003	Huster, et al.			
	2	2003/006462	1/2003	Quek, et al.			
	3	5,640,035	6/1997	Sudo, et al.			
	4	6,180,441	1/2001	Yue, et al.			
	5	5,607,881	3/1997	Huang, Jenn M.			
	6	2001/0031534	10/2001	Ahn, et al.			

FOREIGN PATENT DOCUMENT								
Exam. Initials		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1	EP 0401 174	12/1990	EPO				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	1	
	2	

EXAMINER:	DATE CONSIDERED:
EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.	
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